

Press release
For release: 19/06/2012

PRACE Prototypes evaluation in ISC'12 BOF session

PRACE Future Technologies Evaluation Results
Tuesday, June 19, 2012, 9:00 - 9:45
Hall C2.1 - Congress Center Hamburg

The Partnership for Advance Computing in Europe (PRACE) explores a set of prototypes to test and evaluate promising new technologies for future multi-Petaflop/s systems. These include GPUs, ARM processors, DSPs and FPGAs as well as novel I/O solutions and hot water cooling. A common goal of all prototypes is to evaluate energy-consumption in terms of "energy-to-solution" to be able to estimate the suitability of those components for future high-end systems. The "Future Technologies" work package has developed an energy-to-solution benchmark suite. A synopsis of the assessments and selected results will be presented in a short series of presentations and discussions ISC BOF session on Tuesday 19.6. 2012, at 9:00 - 9:45, Hall C2.1 - Congress Center Hamburg

Next Six PRACE-2IP Prototypes selected

The latest six PRACE prototypes address three main focus areas, namely, Resilience, Node Accelerators, and Low-Power Alternatives to current HPC architecture. They also have different influence and impact on HPC systems. Some results are directly applicable to current systems, others will affect the next generation of supercomputers, and some are looking to evaluate technologies that will need multiple iterations to become viable HPC alternatives to current hardware paradigms.

Further information about the prototypes is available at:

<http://www.prace-ri.eu/PRACE-Prototypes>

About PRACE: The Partnership for Advanced Computing in Europe (PRACE) is an international non-profit association with its seat in Brussels. The PRACE Research Infrastructure (RI) provides a persistent world-class High Performance Computing (HPC) service for scientists and researchers from academia and industry. The Implementation Phase of PRACE receives funding from the EU's Seventh Framework Programme (FP7/2007-2013) under grant agreements n° RI-261557 and n° RI-283493.

For more information, please contact:

Torsten Wilde

High Performance Systems
Leibniz Supercomputing Centre
Boltzmannstrasse 1, 85748 Garching, Germany
Tel.: ++49-89-35831-8726
Fax: ++49-89-35831-8526
Email: Torsten.Wilde@lrz.de